

DEC 13 2006

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December 13, 2006

To: M/S: AMENDMENT**Telephone:****Fax Number:**Examiner Ori NADAV
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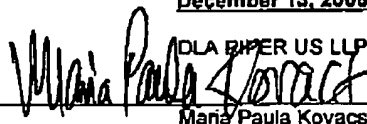
571-272-1660

571-273-8300

From: Jon Y. Ikegami
Reg. No. 51,115
Tel: 650-833-2104Client-Matter Number: 352003-991320
(2102487)Re: U.S. Patent Application No. 10/762,156
Filed: January 20, 2004
Entitled: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING AN OBLIQUE GLOBAL
SIGNAL WIRING AND SEMICONDUCTOR INTEGRATED CIRCUIT WIRING METHOD
Inventors: Naohito KOJIMA et al.Pages: 9 - (including this form)Originals: ☐ will be mailed ☒ will not be mailed**Message:**

Attached are the following documents:

1. Transmittal (PTO/SB/21)(1 pg.); and
2. Second Response To Office Action of September 13, 2006 (7 pgs.).

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DEC 13 2006

PTO/SB/21 (09-04)


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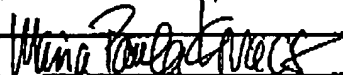
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/762,156	
	Filing Date	January 20, 2004	
	First Named Inventor	Naohito KOJIMA et al.	
	Art Unit	2811	
	Examiner Name	Orl NADAV	
Total Number of Pages in This Submission	9	Attorney Docket Number	352003-991320 (2102487)

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form (+1) <input type="checkbox"/> Fee Attached DEPOSIT ACCT <input checked="" type="checkbox"/> Amendment/Response to 9-11-06 OA <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation <input type="checkbox"/> Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____ <input type="checkbox"/> Landscape Table on CD	<input type="checkbox"/> After Allowance Communication to TC <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): 1. Fax Transmission Cover sheet; 2. Response to Office Action (7 pgs).
Remarks The Commissioner is authorized to charge any additional fees which may be required, including petition fees and extension of time fees, to Deposit Account No. 07-1896 (Docket No. 352003-991320).		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT			
Firm Name	DLA Piper US LLP		
Signature			
Printed name	Jon Y. Ikegami		
Date	December 13, 2006	Reg. No.	51,115

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Signature			
Typed or printed name	Maria Paula Kovacs	Date	December 13, 2006

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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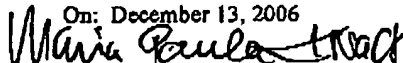
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On: December 13, 2006



Maria Paula Kovacs

Appl. No. : 10/762,156 Confirmation No. 4963
Applicant : Naohito KOJIMA
Filed : January 20, 2004
TC/A.U. : 2811
Examiner : Steven Ho Yin LOKE

Docket No. : 2102487-991320 (352003)
Customer : 26379
No.

Title: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING AN OBLIQUE
GLOBAL SIGNAL WIRING AND SEMICONDUCTOR INTEGRATED
CIRCUIT WIRING METHOD

RESPONSE TO OFFICE ACTION

M/S: AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants request the Examiner to consider the following Response in reply to the Office Action, mailed on September 13, 2006, for the above-referenced application.

Amendments to the Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.